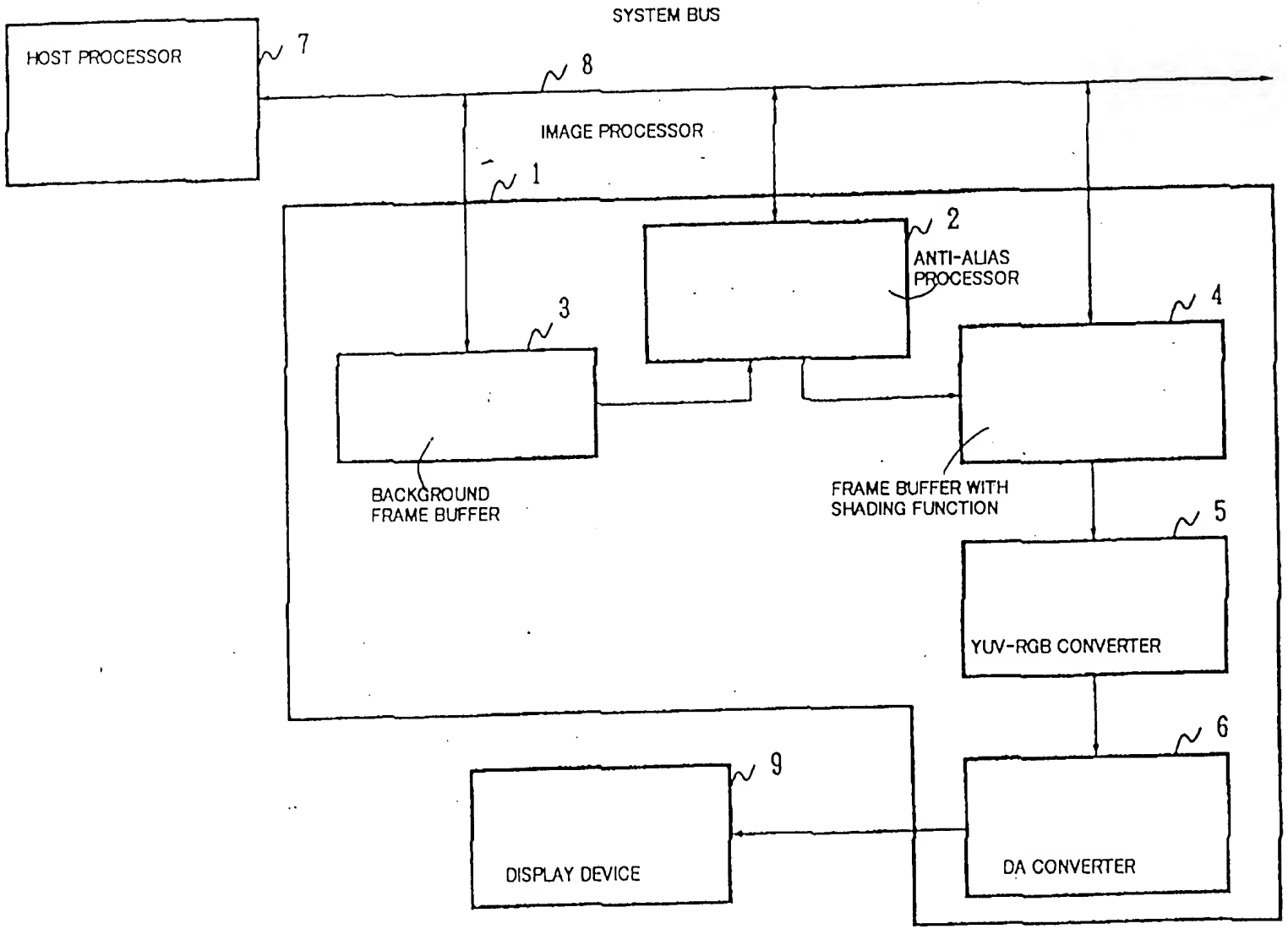

SYSTEM BUS
HOST PROCESSOR
7
8
IMAGE PROCESSOR
1
2
ANTI-ALIAS PROCESSOR
3
4
BACKGROUND FRAME BUFFER
FRAME BUFFER WITH SHADING FUNCTION
5
YUV-RGB CONVERTER
6
9
DISPLAY DEVICE
DA CONVERTER

Fig. 1

JP05265950A-19931015

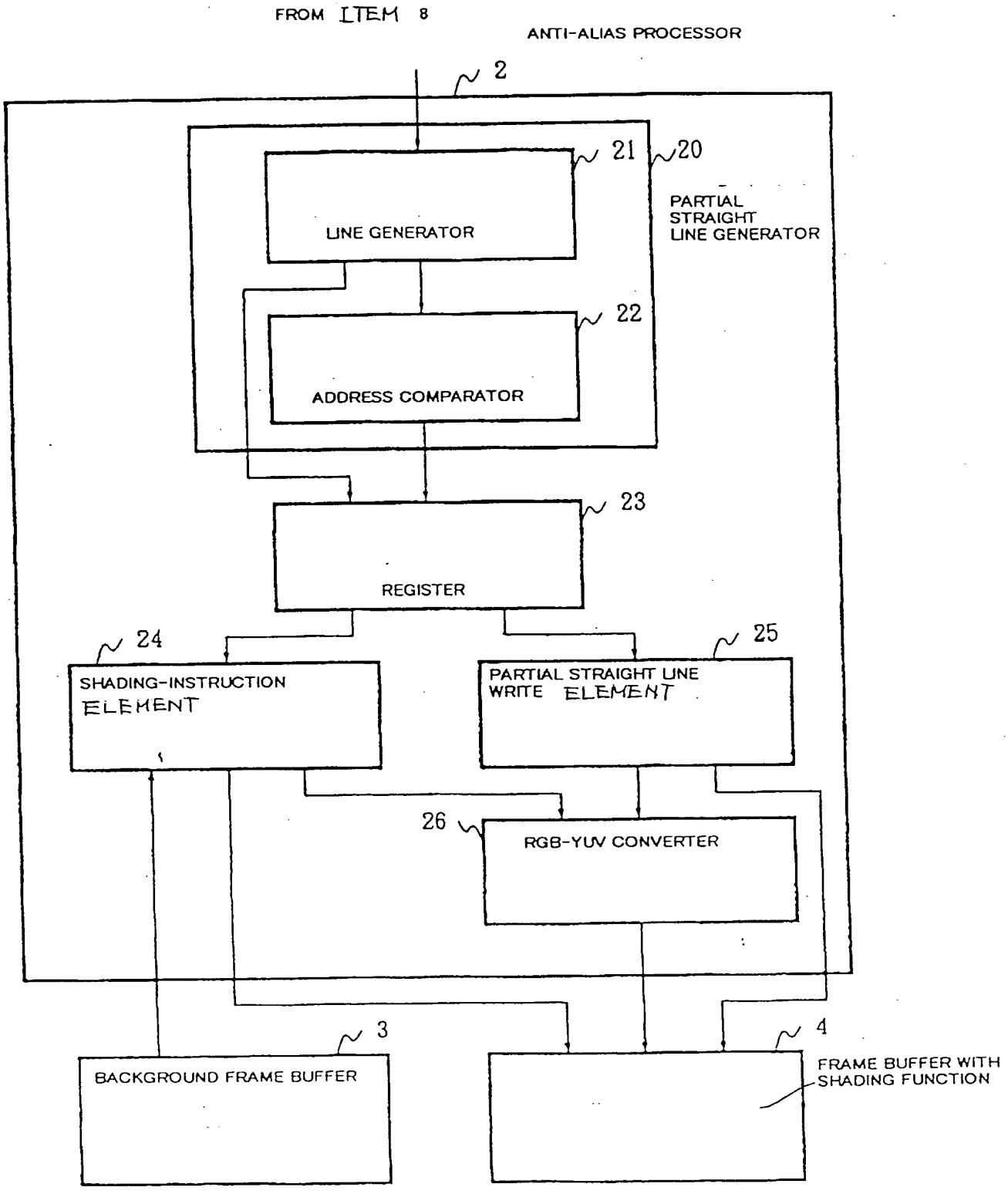


Fig. 2

EP 0 525 960 A1

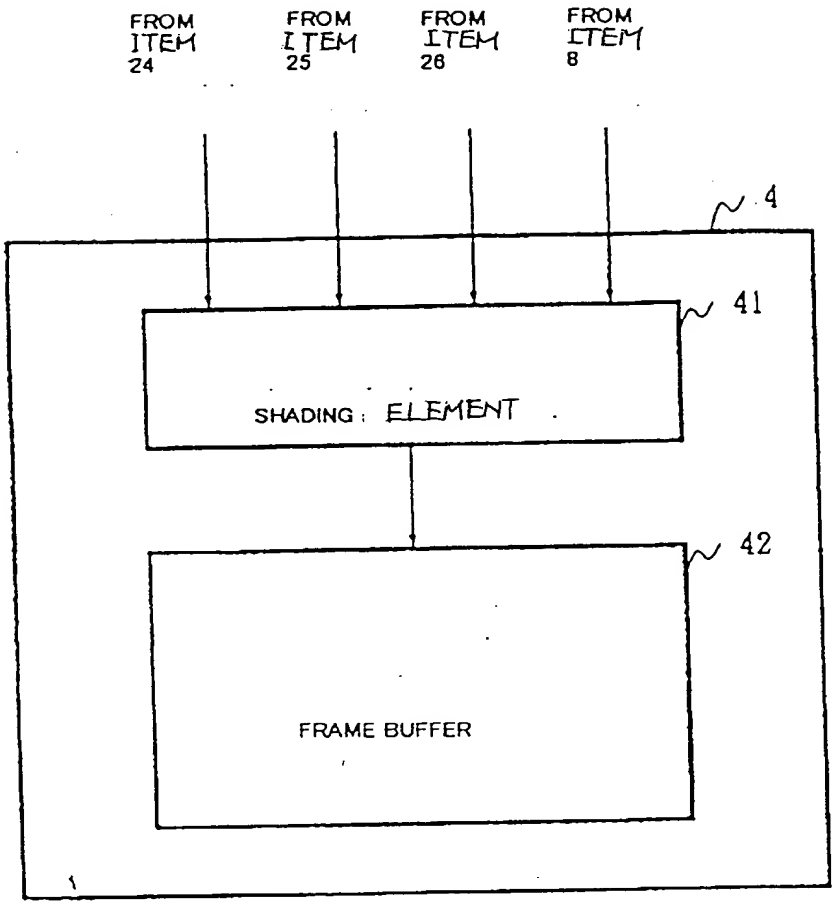


Fig. 3

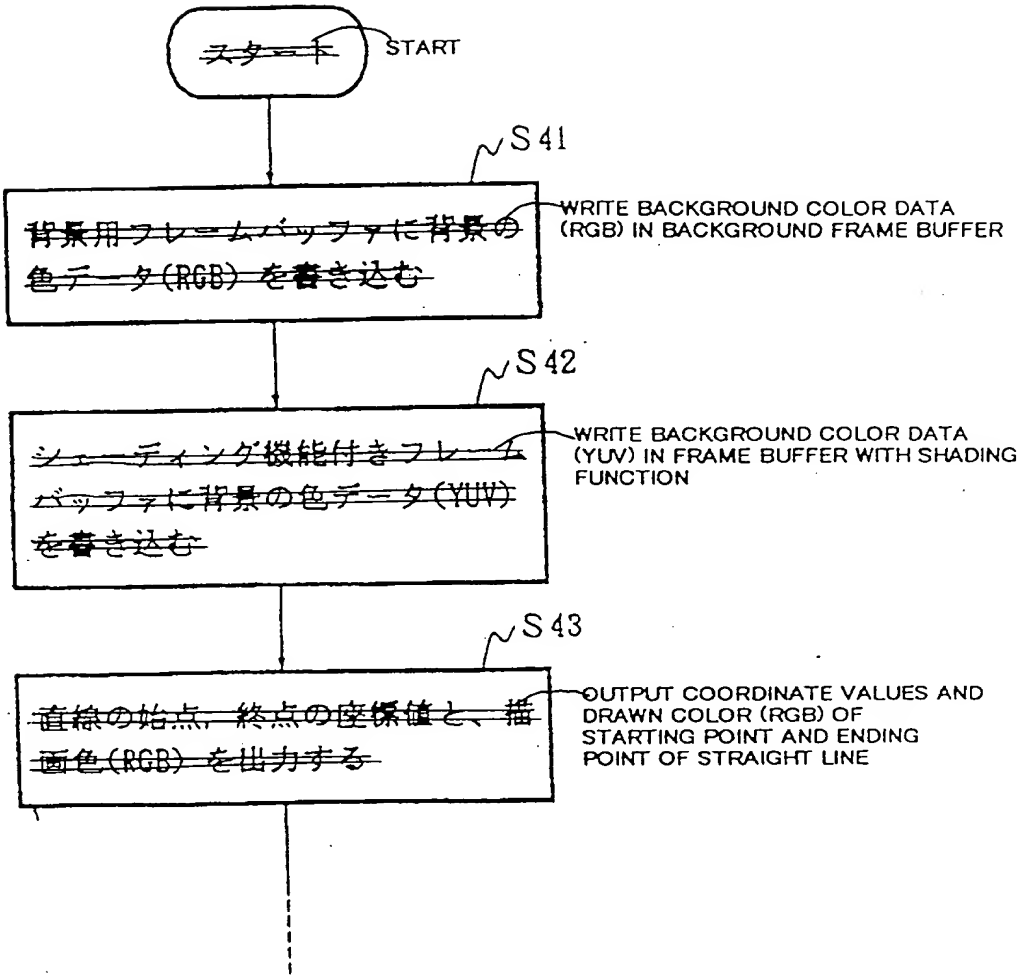

START
S41
WRITE BACKGROUND COLOR DATA (RGB) IN BACKGROUND FRAME BUFFER
S42
WRITE BACKGROUND COLOR DATA (YUV) IN FRAME BUFFER WITH SHADING FUNCTION
S43
OUTPUT COORDINATE VALUES AND DRAWN COLOR (RGB) OF STARTING POINT AND ENDING POINT OF STRAIGHT LINE

Fig. 4

JP05059850A

Fig. 5(a)

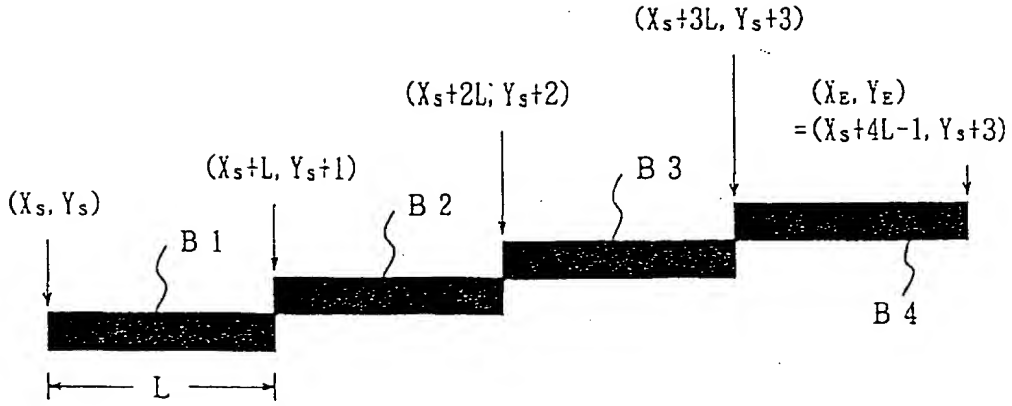
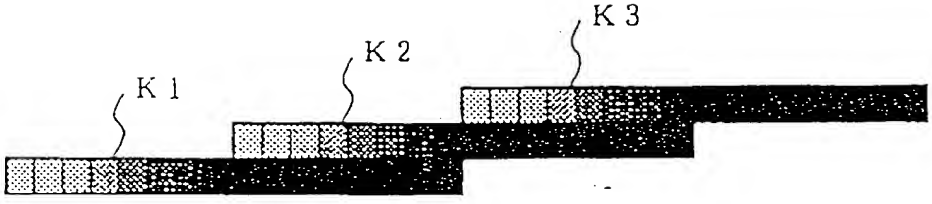


Fig. 5(b)

Fig. 5(c)

Fig. 5(d)



23 REGISTER

STARTING POINT	ENDING POINT	COLOR DATA (RGB)
X_s, Y_s	X_s+L-1, Y_s	(R_s, G_s, B_s)
X_s+L, Y_s+1	X_s+2L-1, Y_s+1	
X_s+2L, Y_s+2	X_s+3L-1, Y_s+2	
X_s+3L, Y_s+3	X_s+4L-1, Y_s+3	

Fig. 6

JP63-305574